

#### **New Product**

Vishay Semiconductors formerly General Semiconductor

# **Bidirectional Surface Mount ThyZorb® Thyristor Overvoltage Protectors**

**DO-214AA (SMB)** 

**Symbol** 

Stand-off Voltage 55 to 230V
Breakover Voltage 80 to 350V
Peak Pulse Current 100A (10/1000µs)
300A (8/20µs)
Holding Current 150mA minimum



### **Mechanical Data**

Case: JEDEC DO-214AA molded plastic body over

passivated junction

Terminals: Solder plated, solderable per MIL-STD-750,

Method 2026

High temperature soldering guaranteed:

250°C/10 seconds at terminals

Mounting Position: Any

Weight: 0.003 ounces, 0.093 gram

#### **Features**

- · Bidirectional crowbar protection
- Complies with Bellcore TR-NWT-001089, and IEC-1000-4-5 standards
- Series is designed to protect telecommunication equipment against lightening and AC induced transients
- Plastic package has UL Flammability Classification 94V-0
- Low profile package with built-in strain relief for surface mounted applications

### Maximum Ratings and Thermal Characteristics TA= 25°C unless otherwise noted.

| Parameter   |                     | Symbol           | Value       | Unit  |
|---|---------------------|------------------|-------------|-------|
| Power Dissipation   | TL = 50°C           | Р                | 5           | W     |
| Peak Pulse Current  | 10/1000μs<br>8/20μs | lpp              | 100<br>300  | А     |
| Non-repetitive surge peak on-state current                                | tp = 20ms           | ITSM             | 55          | А     |
| Critical rate of rise of off-state voltage (VRM)                          |                     | dV/dt            | 5           | KV/μs |
| Storage temperature range   |                     | T <sub>stg</sub> | -55 to +150 | °C    |
| Maximum junction temperature  |                     | Tj               | 150         | °C    |
| Thermal resistance junction to leads                                      |                     | Røjl             | 100         | °C/W  |
| Thermal resistance junction to ambient on P.C with recommended pad layout | RөJA                | 20               | °C/W        |       |

### **IPP Ratings for the Following Surge Standards:**

| Standard     | Waveform           | Ірр               |  |  |
|--------------|--------------------|-------------------|--|--|
| GR-1089-CORE | 2/10µs             | 500A              |  |  |
| IEC61000-4-5 | 8/20µs             | 300A+             |  |  |
| FCC Part 68  | 10/160 <i>μ</i> s  | 250A+             |  |  |
| ITU-TK20/21  | 10/700 <i>μ</i> s  | 200A <sup>+</sup> |  |  |
| FCC Part 68  | 10/560 <i>µ</i> s  | 160A+             |  |  |
| GR-1089-CORE | 10/1000 <i>µ</i> s | 100A              |  |  |

Values with \* have improved IPP specs over equivalent competitor part numbers

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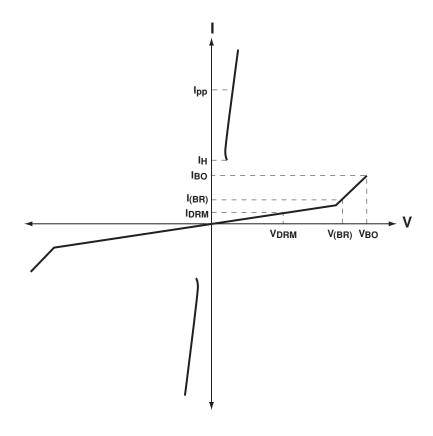
### Electrical Characteristics (TA = 25°C unless otherwise noted)

| Туре         | Device<br>Marking<br>Code | Maximum<br>I <sub>R</sub> @ V <sub>R</sub> | Vr  | Stand-off<br>Voltage<br>VDRM (V) | VDRM | Maximum<br>Breakover<br>Voltage<br>VBO (V) <sup>(1)(3)</sup> | Maximum<br>Breakover<br>Current<br>IBO (mA) <sup>(1)</sup> | Minimum<br>Holding<br>Current<br>IH (mA) | Typical<br>Capacitance<br>C (pF) <sup>(2)</sup> |
|--------------|---------------------------|--|-----|----------------------------------|------|--|--|--|---|
| SMP100LC-65  | L06                       | 50   | 65  | 55                               | 2.0  | 80*  | 800  | 150                                      | 120   |
| SMP100LC-90  | L09                       | 50   | 90  | 81                               | 2.0  | 115*   | 800  | 150                                      | 76  |
| SMP100LC-120 | L12                       | 50   | 120 | 108                              | 2.0  | 145*   | 800  | 150                                      | 70  |
| SMP100LC-130 | L13                       | 50   | 130 | 117                              | 2.0  | 165*   | 800  | 150                                      | 70  |
| SMP100LC-140 | L14                       | 50   | 140 | 120                              | 2.0  | 180*   | 800  | 150                                      | 65  |
| SMP100LC-160 | L16                       | 50   | 160 | 144                              | 2.0  | 220  | 800  | 150                                      | 65  |
| SMP100LC-200 | L20                       | 50   | 200 | 170                              | 2.0  | 265*   | 800  | 150                                      | 65  |
| SMP100LC-230 | L23                       | 50   | 230 | 200                              | 2.0  | 300*   | 800  | 150                                      | 60  |
| SMP100LC-270 | L27                       | 50   | 262 | 230                              | 2.0  | 350*   | 800  | 150                                      | 60  |

Notes: (1)  $dv/dt \le 2V/\mu s$ 

(2)  $V_R = 2V$ , f = 1MHz

(3) Values with \* have improved VBO specs over equivalent competitor part numbers



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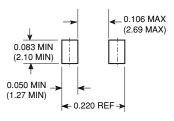


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## 0.086 (2.20) 0.077 (1.95) 0.180 (4.57) 0.180 (4.06) 0.096 (2.44) 0.084 (2.13) 0.008 (0.203) 0.008 (0.203) 0.008 (0.203) 0.008 (0.203) 0.008 (0.203) 0.008 (0.203)

0.220 (5.59) 0.205 (5.21)

#### **Mounting Pad Layout**



Dimensions in inches and (millimeters)



Vishay

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